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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of: Docket No. P27154

Michael P. Belyansky, et al. Confirmation No. 2429

Group Art Unit: 2818

Appin. No. : 10/708,430

Examiner: A. Huynh

Filed: March 3, 2004

For : MOBILITY ENHANCED CMOS DEVICES

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
U.S. Patent and Trademark Office
Customer Service Window, Mail Stop____
Randolph Building
401 Dulany Street
Alexandria VA 22314

Sir:

In accordance with the duty of disclosure under 37 C.F.R. § 1.56, and supplemental to the Information Disclosure Statement filed on December 13, 2005, applicant respectfully brings the following documents, listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application.

Further to the U.S. Patent and Trademark Office's decision to waive the requirement under 37 C.F.R. § 1.98 (a)(2)(i), copies of the U.S. patents and U.S. published patent applications are not enclosed herewith. However, if any copies are needed, the Examiner is respectfully requested to contact the undersigned.

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Applicants respectfully request that the Examiner consider the materials cited and indicate such consideration by appropriately initialing the enclosed PTO-1449 Form and including a copy of the initialed form in the next official communication.

Applicants note that a Petition to Withdraw the Application from issue and a Request for Continued Examination (RCE) have been filed with the Office of Petitions on even date. Therefore, copies these documents are enclosed.

Applicants acknowledge that this Supplemental Information Disclosure Statement (SIDS) is being filed after payment of the issue fee. However, Applicants are concurrently filing this SIDS as a submission to the filing of a Request for Continued Examination (RCE) along with the fee for filing an RCE, and a petition to request withdrawal of the application from issue. Accordingly, Applicant respectfully requests consideration of the above-noted documents.

Should there be any questions concerning this application, the Examiner is invited to contact the undersigned at the below listed telephone number.

Respectfully submitted, Michael P. Belyansky, et al

Andrew M. Calderon Reg. No. 38,093

GREENBLUM & BERNSTEIN, P.L.C. 1950 Roland Clarke Place Reston, VA 20191 (703) 716-1191

		partment of Commerce t and Trademark Office		Atty. Dock P27154	et No.	Applic	Application No.		
INFO		Applicant Michael P. Belyansky et al.							
BY APPLICANT (Use several sheets if necessary)				Filing Date March 3, 20		Group 2818	-		
		U.S. PATENT	DOCUMI	IENTS			P4-20		
EXAMINER						0.117.01.100	i i	FILING DATE IF APPROPRIATE	
INITIAL	DOCUMENT NUMBER	DATE	NAME Armstrong et al. Yeh et al. Saitoh Deshpande et al. Doris et al. Chidambarrao et al. Doris et al. Belyansky et al.		CLASS	SUBCLASS	IF APPRO	JPRIATE	
	US 2002/0063292 A1	5-30-2002							
	US 2003/0032261 A1	2-13-2003					-		
	US 2003/0040158 A1	2-27-2003							
	US 2004/0238914 A1	12-2-2004						*****	
	US 2004/0262784 A1	12-30-2004							
	US 2005/0040460 A1	2-24-2005						<u></u>	
	US 2005/0082634 A1	4-21-2005							
	US 2005/0093030 A1	5-5-2005							
	US 2005/0098829 A1	5-12-2005							
	US 2005/0106799 A1	5-19-2005							
	US 2005/0145954 A1	7-7-2005						.	
	US 2005/0148146 A1	7-7-2005							
	US 2005/0194699 A1	9-8-2005							
	US 2005/0236668 A1	10-27-2005	Zhu et	al.					
	US 2005/0245017 A1	11-3-2005	Belyan	sky et al.					
	US 2005/0280051 A1	12-22-2005	Chidambarrao et al. Belyansky et al. Doris et al. Doris et al. Doris et al.						
	US 2005/0282325 A1	12-22-2005							
	US 2006/0027868 A1	2-9-2006							
-	US 2006/0057787 A1	3-16-2006							
	US 2006/0060925 A1	3-23-2006							
	6,483,171	11-19-2002	Forbes	et al.					
	6,831,292	12-14-2004	Currie	et al.					
	6,717,216	4-6-2004	Doris e	et al.					
	6,825,529	11-30-2004	Chidar	nbarrao et al.		<u> </u>			
	7,015,082	3-21-2006	Doris	et al.					
	6,974,981	12-13-2005	Chidambarrao et al.		,		 	1 t 2 t 2 t 2 t 2 t 2 t 2 t 2 t 2 t 2 t	
	6,977,194	12-20-2005	Belyan	sky et al.					
<u>I</u>		FOREIGN PATI	1	······		<u> </u>			
1 1								LATION	
	DOCUMENT NUMBER	DATE		INTRY	CLASS	SUBCLASS	YES	NO	
	JP 64-76755	3-22-1989	Japan				<u> </u>	X	
	OTHER DOCUM	ENTS (Including A	uthor, Titl	e, Date, Pertino	ent Pages, Etc.)				
					/ fc // 12 %				
EXAMINER		_ 4 - 4		TE CONSIL			1. ** -*		
	tial if citation considered, whethe not considered. Include copy of t					; araw line thi	ougn citatio	n ii not in	

FORM PTO-1449	•	U.S. Department of Commerce Patent and Trademark Office			ket No.	Appli	Application No.				
INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Applicant						
(Use several sheets if necessary)				Filing Date Gr			Group				
		U.S. PATEN	T DOCUMI	ENTS	-						
EXAMINER INITIAL	DOCUMENT NUMBER	DATE		NAME	CLASS	SUBCLASS	FILING DATE UBCLASS IF APPROPRIATE				
		FOREIGN PAT	ENT DOCU	IMENTS				A-48-1-48-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1			
	DOCUMENT NUMBER	DATE	cou	OUNTRY CLASS SUBCLASS		TRANSLATI YES	ON NO				
	OTHER DOCUMEN	NTS (Including A	Author, Title	e, Date, Pertin	ent Pages, Etc.)					
	G. Zhang, et al., "A New 'M Bipolar Transistors." IEEE T										
	H.S. Momose, et al., "Tempe Mechanism Analyzed by MC					_	on and its				
	C.J. Huang, et al., "Temperature Dependence and Post-Stress Recovery of Hot Electron Degradation Effects in Bipolar Transistors." IEEE 1991, Bipolar Circuits and Technology Meeting 7.5, pp. 170-173.										
	S.R. Sheng, et al., "Degradation and Recovery of SiGe HBTs Following Radiation and Hot-Carrier Stressing." pp. 14-15.										
	Z. Yang, et al., "Avalanche Current Induced Hot Carrier Degradation in 200 GHz SiGe Heterojunction Bipolar Transistors." pp. 1-5.										
	H. Li, et al., "Design of W-B Automotive Radar Systems."	and VCOs wi	_	•		l Application	in 77 GHz				
	· · · · ·	H. Wurzer, et al., "Annealing of Degraded non-Transistors-Mechanisms and Modeling." IEEE Transactions on Electron Devices, vol. 41, no. 4, April 1994, pp. 533-38.									
	¥ ' '	B. Doyle, et al., "Recovery of Hot-Carrier Damage in Reoxidized Nitrided Oxide MOSFETs." IEEE Electron Device Letters, vol. 13, no. 1, January 1992, pp. 38-40									
	H.S. Momose, et al. "Analys Bipolar Transistors for Bi-Cl 978-987.	and the second s					the state of the s				
	M. Khater, et al., "SiGe HBT Technology with Fmax/Ft = 350/300 GHz and Gate Delay Below 3.3 ps". 2004 IEEE, 4 pages.										
	J.C. Bean, et al., "GEx SI 1-x/Si Strained-Layer Superlattice Grown by Molecular Beam Epitaxy". J. Vac. Sci. Technol. A 2(2), AprJune 1984, pp. 436-440.										
	J.H. Van Der Merwe, "Regular Articles". Journal of Applied Physics, Volume 34, No. 1, January 1963, pp. 117-122.										
	J.W. Matthews, et al., "Defects in Epitaxial Multilayers". Journal of Crystal Growth 27 (1974), pp. 118-125.										
***************************************	Subramanian S. Iyer, et al. "Heterojuction Bipolar Transistors Using Si-Ge Alloys". IEEE Transactions on Electron Devices, Vol. 36, No. 10, October 1989, pp. 2043-2064										
the control of the co	R.H.M. Van De Leur, et al., 'Superlattices'. J. Appl. Phys.			•		of Si/Ge Alloy	s and				
	D.C. Houghton, et al., "Equilibrium Phys. Lett. 56 (5), 29 January			ss for SI 1-	x GEx Strain	ed Layers on	(100) Si". Appl.				
	Q. Quyang et al., "Two-Dime Device Performance and Sca	ensional Band	lgap Engir	_		Ge pMOSFET	with Enhanced				
EXAMINER			DA	TE CONSI	DERED			<i></i>			
	tial if citation considered, whether					9; draw line th	rough citation if	not in			
conformance and n	not considered. Include copy of this	s torm with no	ext commu	inication to	applicant.						